

Fuzzy Controller Based High Power Grid Connected 11-Level Hybrid Multilevel Inverter for Wind farms

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Abstract-This paper proposes a single phase eleven level diode clamped inverter with reduced number of switches to achieve desired level of output voltage for wind farm applications. Considerable switching losses and the gating requirements have been reduced by selecting less number of switches to achieve the desired level of output. The control logic for the inverter is suitably designed with the help of fuzzy logic controller with proper set of fuzzy rules and decision table. The sinusoidal output is obtained by properly filtering the output of inverter and connected to grid. The simulation of the entire circuit has been carried out in MATLAB Simulink to validate the results and also the harmonic profile also studied.

Index Terms - Wind Generator(WG), Sinusoidal Pulse Width Modulation (SPWM), Multilevel Inverter (MLI), Total Harmonic Distortion(THD), Wind Energy Conversion System(WECS)

I.INTRODUCTION

Wind power is undergoing the fastest rate of growth than any other form of electricity generation in the world. The low environmental impact of wind energy makes it a very attractive solution. The resource potential is large. Integration of wind power plant into the electric power system presents challenges primarily due to the natural characteristic of the wind plants which differ in some respect from the conventional plants[1]. A typical wind energy conversion system includes a wind turbine, interconnection apparatus, control systems and generators. Variable speed wind turbines are capable of producing 9% to 15% more energy output as compared to their constant speed. But it necessitates the need for power electronic converters to provide a fixed frequency and fixed voltage power to their loads.

The most advanced generator type used for wind energy conversion system perhaps the permanent-magnet synchronous generator (PMSG). This machine offers, best efficiency compared to the same power level and machine size the best efficiency among all types of machines. With high robustness in construction, easy maintenance due to slip ring permanent magnet which supplies rotor flux in synchronous

machines without excitation loss supports the wind power generation development.[3] The Electrical power output of the PMSG cannot be delivered directly to the grid. Power electronics converters are used to overcome this limitation. The main disadvantage of the PMSG is the high cost of the PM material and power converter. The three-level converter has been widely studied in literature but the application of diode-clamped converters with higher (four or more) levels has not been analyzed for the production of wind power.

In this paper, a diode clamped multilevel inverter is cascaded with the H-bridge forming a hybrid topology[4]. The DC input to the inverter is fed from four independent wind generators. The controlled rectifier connected at the output of each generator gives the controlled DC and is maintained constant by means of regulator. This constant obtained from each is given as the input of inverter[2]. The output voltage of the inverter is controlled by generating pulse from the control circuit. A Fuzzy Logic controller is used for the purpose. Fuzzy logic control enables better control by controlling the harmonics[10]. The Harmonic profile also been monitored and presented. A suitable logic developed in the fuzzy eliminates the harmonics and reduced the THD. The rest of the paper is organized as follows section II describes the wind generator powered multilevel inverter . Section III describes Pulse Width Modulation . Section IV describes Fuzzy Logic Controller ,Simulation results are discussed in section V, Section VI FFT Analysis and Finally, concluding remarks are given in section VII.

II WIND GENERATOR POWERED MULTILEVEL INVERTER

In order to meet the demand, a wind power with high quality is obtained using the converter, a multilevel converters are good alternative to the conventional converters for this systems. A multilevel converter enables the ac voltage to be increased without an output transformer, reducing the output voltage and currents harmonic content and make the output waveform closer to sine wave.[6] In addition, the cancellation of low frequency harmonics from the ac voltages at the different levels means that the size of the ac inductance can be reduced ,thus a consequent decrement in the expenses of the overall system.

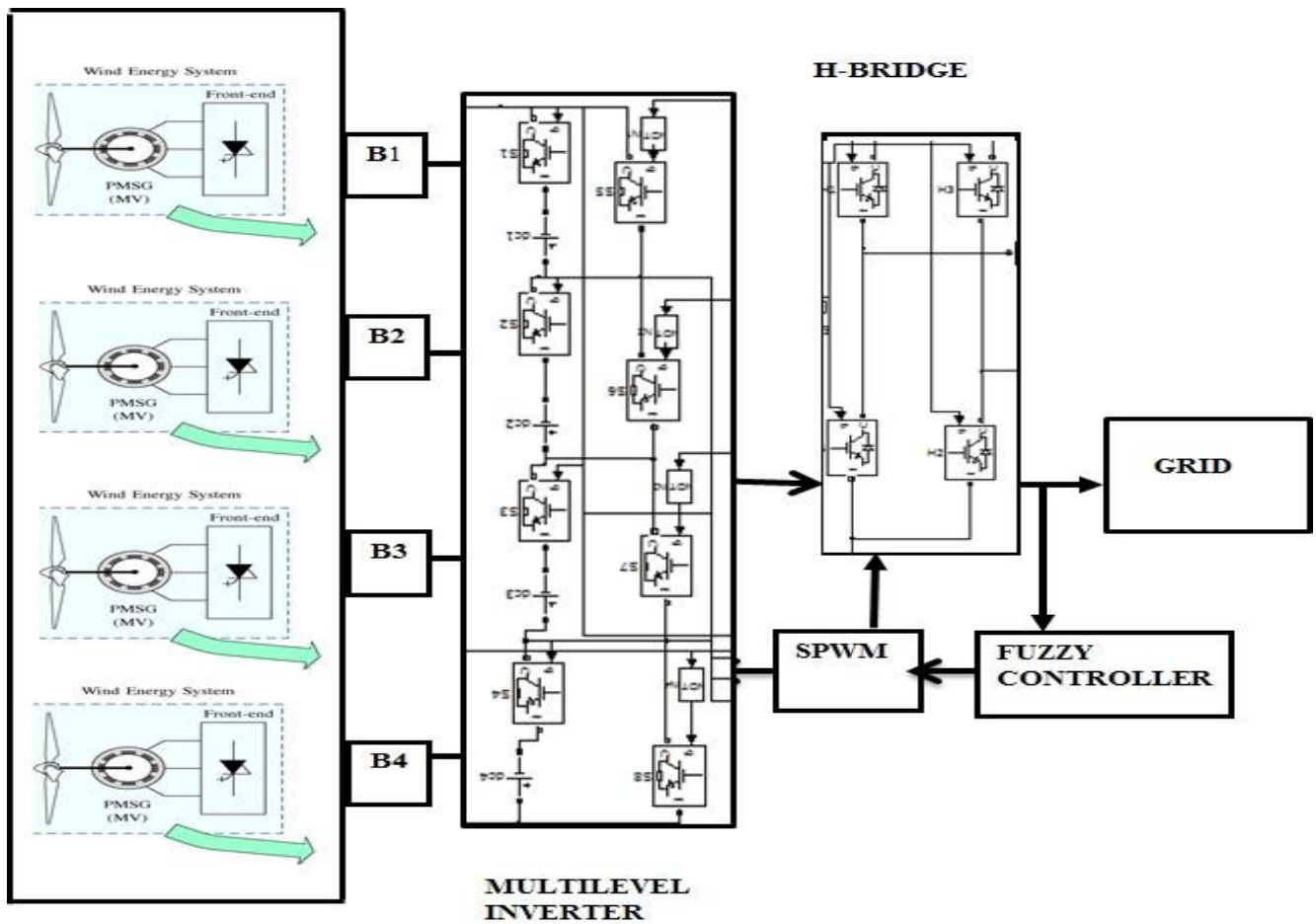


Fig.1. Block Diagram of Overall System

The block diagram for the constant output multilevel inverter is shown in figure 1. In this diagram it consists of wind farm, an inverter, fuzzy logic controller and sinusoidal pulse width modulation block. Four DC source voltage is given as input to the hybrid inverter which combines the diode clamped multilevel inverter and H-bridge inverter. By using twelve switches in hybrid multilevel inverter with each switch having different voltage to generate the eleven step voltage of symmetrical output. The eleven step output is applied to fuzzy controller to maintain the constant output, by giving reference voltage to the fuzzy logic controller. If there any deviation in output, the controller is used to compensate the output voltage and SPWM signal for the switch is varied. The constant dc supply for the inverter is from the wind farm and the pulses for each switch are obtained from Sinusoidal Pulse Width Modulation Technique. The Diode Clamped Inverter switching states is given in the Table 1. The number of output phase voltage level is defined by $m=2s+1$, where s is the number of DC source.

TABLE 1
SWITCHING STATES OF DIODE CLAMPED INVERTER

SWITCHES								
LEVEL	S4	S3	S2	S1	S4'	S3'	S2'	S1'
I	ON	OFF	OFF	OFF	OFF	ON	ON	ON
II	ON	ON	OFF	OFF	OFF	OFF	ON	ON
III	ON	ON	ON	OFF	OFF	OFF	OFF	ON
IV	ON	ON	ON	ON	OFF	OFF	OFF	OFF
-I	ON	ON	ON	OFF	OFF	OFF	OFF	ON
-II	ON	ON	OFF	OFF	OFF	OFF	ON	ON
-III	ON	OFF	OFF	OFF	OFF	ON	ON	ON
-IV	OFF	OFF	OFF	OFF	ON	ON	ON	ON

The term H bridge is derived from the typical graphical representation circuit. An H bridge is built with four switches (solid-state or mechanical). When the switches S1 and S4 are closed (and S2 and S3 are open) a positive voltage will be obtained across the load. By opening S1 and S4 switches and closing S2 and S3 switches, this voltage is reversed, allowing reverse voltage across the load. Using the nomenclature above, the switches S1 and S2 should never be closed at the same time, as this would cause a short circuit on the input voltage source. The same applies to the switches S3 and S4[9]. This condition is known as shoot-through.

III PULSE WIDTH MODULATION

In sinusoidal PWM instead of maintaining the width of all pulses the same as in the case of multiple PWM, the width of each is varied in proportion to the amplitude of a sine wave evaluated at the same pulse. The distortion is reduced significantly compared to multiple PWM. In order to generate pulses for twelve switches a sinusoidal wave is compared with twelve carrier wave having different amplitude as shown in the figure. A Fuzzy Controller Output is given as an input to the PWM in order to maintain constant output voltage from multilevel inverter.

Inverter output voltage, $V_{ao} = V_{dc}/2$, When $v_{control} > v_{tri}$, and $V_{ao} = -V_{dc}/2$, When $v_{control} < v_{tri}$. PWM frequency is the same as the frequency of v_{tri} . Amplitude is controlled by the peak value of $v_{control}$ and Fundamental frequency is controlled by the frequency of $v_{control}$. Modulation Index (m) is given by :

$$m = \frac{V_{ao}}{V_{dc}/2} = \frac{V_{ao}}{V_{dc}/2} \quad (1)$$

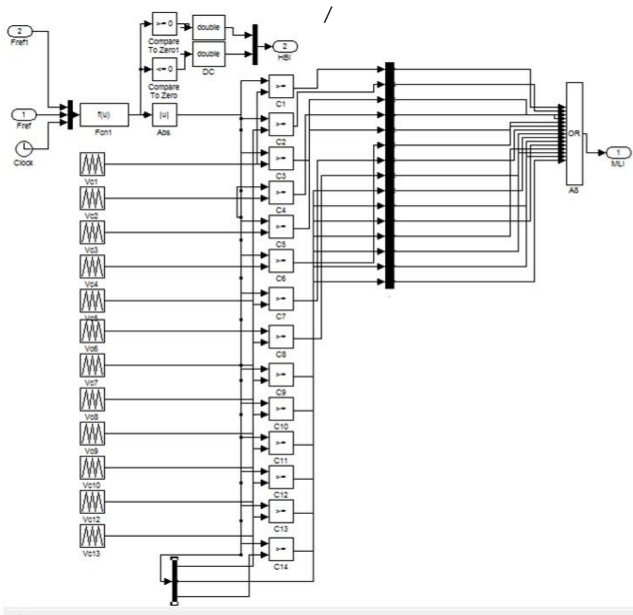


Fig.2 Logic Circuit for Pulse Generation

IV FUZZY LOGIC CONTROLLER

Fuzzy Logic Control uses non-mathematical decision based algorithms that use operators experiences. This Type of control strategy is well suited for non-linear systems. Fuzzy

Logic Control is developed in this work to obtain desired output voltage of the chosen inverter. In order to obtain the fuzzy control surface for non-linear, time varying and complex dynamic system, there are a number of steps to be followed as discussed below. The block diagram of Fuzzy Logic Control scheme developed for the chosen single phase PWM Inverter is shown in the figure 3. The FLC is divided into five modules: Fuzzifier, Database, Rule base, Decision maker and Fuzzifier.

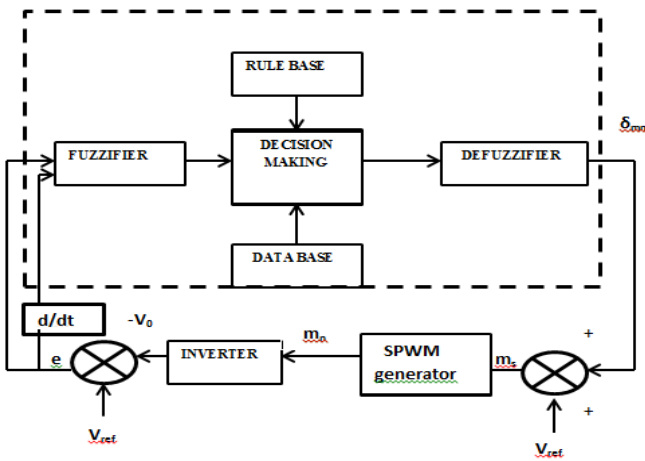


Fig.3 Fuzzy Logic Control Scheme for Single Phase Multilevel Inverter

The inputs to the FLC are the error $e = V_{ref} - V_o$ and the change in error $ce = e_n - e_{n-1}$ where V_o is the actual output voltage of the MLI. V_{ref} is the desired output voltage and subscript n denotes sampling instances. δm_n is the change of modulation index inferred by the FLC at the nth sampling instant using δm_n .

The updated modulated signal m_s obtained and fed to the SPWM generator which provided appropriate PWM signals m_n . The change in modulation index inferred by the FLC at the nth sampling instant. Using this the updated modulated signal is obtained and fed to the SPWM generator which provides appropriate PWM signals. Each universe of discourse is divided into seven fuzzy subsets namely PB(Positive Big), PM(Positive Medium), PS(Positive Small), NM(Negative Medium) and NB(Negative Big).

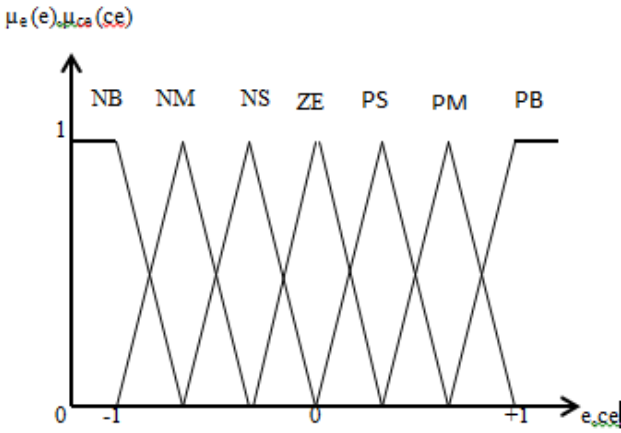


Fig .4 Membership function for e and ce

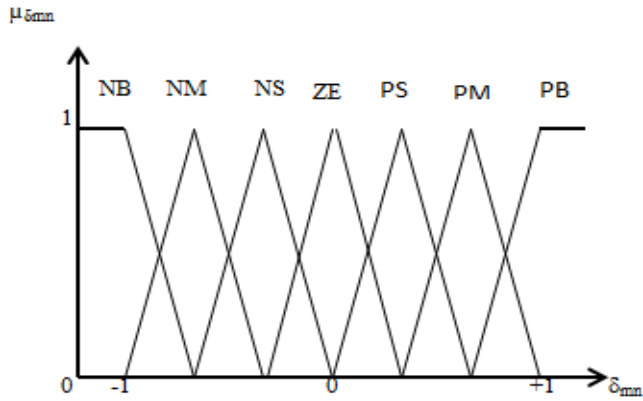


Fig. 5 Membership function for Change in Modulation Index

The derivation of fuzzy control rules for chosen inverter is heuristic in nature and is based on the following criteria:

- When the output of the inverter deviates far from the reference, the change of modulation index must be large so as to bring the output to the reference quickly.
- When the output of the inverter is approaching the reference, a small change of modulation index is necessary.
- When the output of the inverter is near the reference and is approaching it rapidly, the modulation index must be kept constant so as to prevent further deviation.
- When the reference is reached and the output is still changing the modulation index must be changed a little bit to prevent the output from moving away.
- When the reference is reached and the output is steady, the modulation index remains unchanged.
- When the output is larger than the reference, the sign of the change of modulation index must be negative and vice versa.

According to these criteria, a rule base is derived as in table 2.

TABLE II
RULE BASE

e\ce	NB	NM	NS	ZE	PS	PM	PB
NB	NB	NB	NB	NB	NM	NS	ZE
NM	NB	NB	NB	NM	NS	ZE	PS
NS	NB	NB	NM	NS	ZE	PS	PM
ZE	NB	NM	NS	ZE	PS	PM	PB
PS	NM	NS	ZE	PS	PM	PB	PB
PM	NS	ZE	PS	PM	PB	PB	PB
PB	ZE	PS	PM	PB	PB	PB	PB

V SIMULATION RESULTS

The overall simulated system for the proposed system is implemented below and each section is shown separately for better understanding.

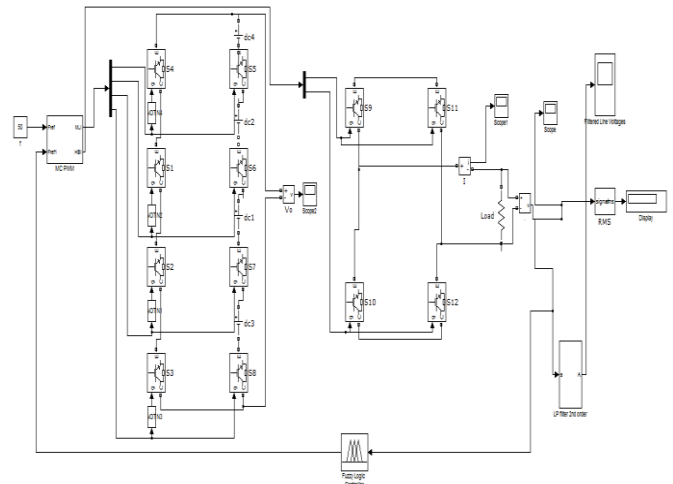


Fig.6 Grid Interface of a PMSG Based Wind Energy Conversion Systems inverter

The system under consideration employs PMSG-based variable speed WECS . The wind turbine converts the power of the wind to mechanical power in the rotor shaft. This is then converted to electricity using a permanent magnet synchronous generator (PMSG). The output voltage is rectified using a three-phase diode bridge rectifier. The result is fed into a PI controller whose output is compared to a triangular waveform to determine when to turn the dc-dc boost converter switch ON or OFF.

The simulation have shown that the developed waveform have less harmonics compared to the conventional system and extracting maximum power from the air stream at any wind speed without the knowledge of wind speed or rotor speed.

A)PMSG Output Voltage

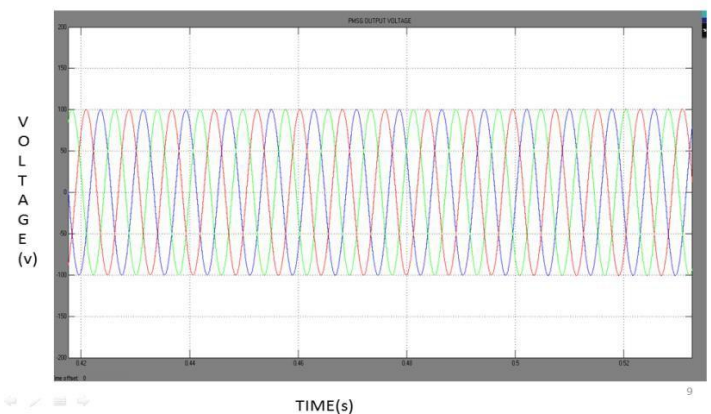


Fig.7.PMSG Output voltage

PMSG generator connected to the wind turbine is simulated and corresponding output voltage is showed in the figure7.In order to make the torque of the turbine equals to the torque of

the generator a drive and train circuit is connected. The output of the system is equal to 100V.

B)Uncontrolled Rectifier Output

The output of the PMSG is given to an Uncontrolled Rectifier in order to convert the ac voltage to dc, the output voltage is not able to control because diodes are used in this rectifier. In order to control the output voltage and maintain it as a constant voltage a boost converter with a PI Controller is used.

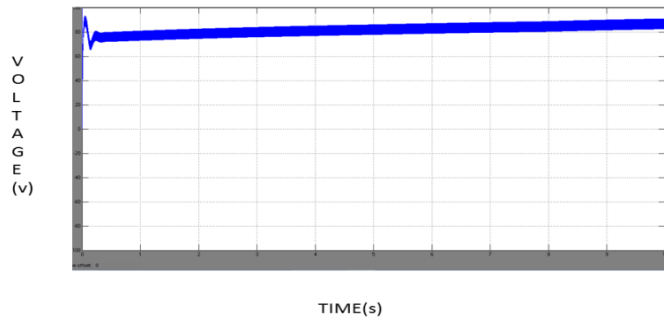


Fig. 8 output voltage of uncontrolled rectifier

C) Boost Converter Output

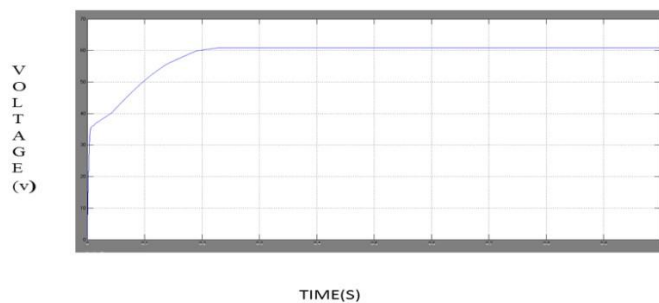


Fig.9.Output voltage of boost converter

A boost converter is used here, because as the wind speed varies the output of the uncontrolled rectifier varies and voltage drop will occur, so in order to give inverter input a constant dc supply a boost converter with PI Controller is used. And thus the output obtained is equal to 60V.

D) Output of Hybrid Multilevel Inverter

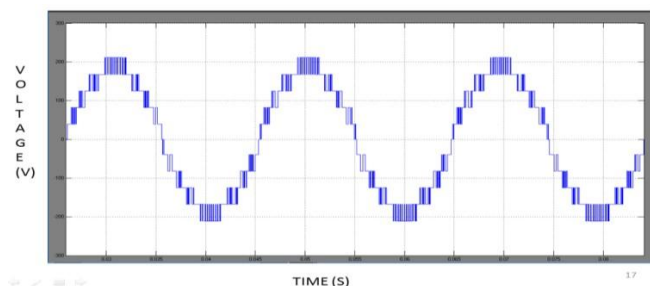


Fig.10. Output Voltage of Symmetric Multilevel Inverter

The H Bridge inverter is connected with the diode clamped multilevel inverter with switches to form a H ybrid topology. The level creator part produce output voltage which

is always positive and the H-bridge part is to change the polarity of the output. Basically the inverter operation is to convert the variable DC into an AC. The input dc source is given by using batteries or photo voltaic cells to the cascaded circuit. Here fuzzy logic controller is used to control the output voltage of the inverter. By using sinusoidal pulse width modulation technique the triggering pulse given to the switches are controlled.

VI FFT ANALYSIS OF OUTPUT

A)Output Voltage without Filter

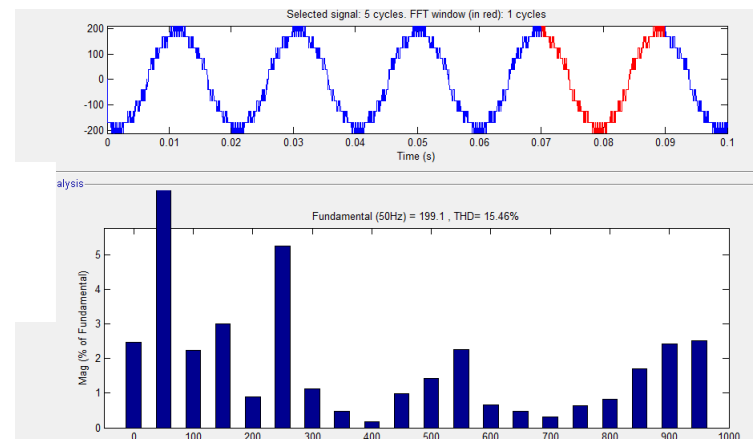


Fig. 11 FFT Analysis

By using the sinusoidal pulse width modulation control we can control the output by changing the magnitude and the modulation index value of the reference and carrier waveform. Mostly the carrier wave is triangular wave and the sampling wave is either we take DC signal as reference or we take sine wave. The gate triggering is very important in the IGBT device compared to many semiconductor power devices IGBT device has the fast switching characteristics and high speed applications. so this device is mostly used in the inverter circuits nowadays. The Total Harmonic Distortion of the multilevel inverter output is equals to 15.46%, which will be more than 20% for conventional inverters by using same number of switches

B) Output Voltage with Filter

In this symmetric multilevel inverter it consists of two parts as level creator part and a H-bridge part. The input voltage to the dc source is 60V. The level creator part produces a output voltage which is always positive and the H-bridge part is to change the polarity of the output. The voltage at the output of the level creator part is about 170V. The output voltage at the output is 240V. The THD get reduced to 5.56% after filtering. The filter inductance L and the filter capacitance C and $L = 560 \mu\text{H}$ with $R_L = 0.34 \Omega$ and $C = 0.5 \mu\text{F}$ with $R_C = 8.64 \Omega$, respectively.

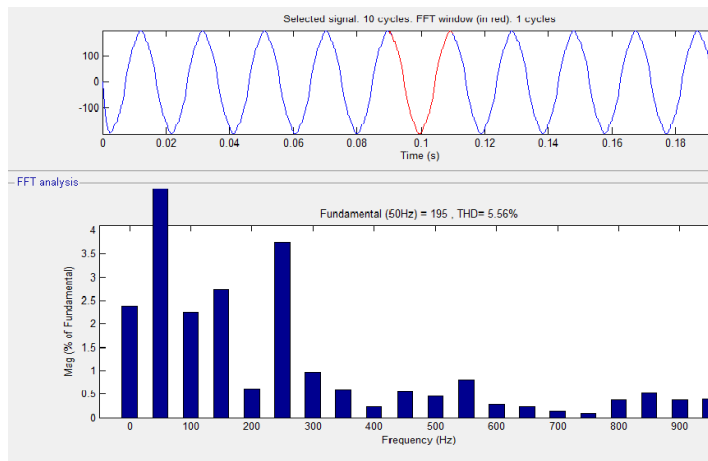


Fig.12 FFT Analysis of Filtered Output

VII CONCLUSION

The performance of PMSG-based variable speed WECS has been demonstrated under varying wind conditions. The grid-side inverter is able to inject the generated power into the grid with harmonic compensation.. A new hybrid topology with fuzzy controller technique for the symmetrical configuration is proposed.

The cascade multilevel inverter with equal DC sources are illustrated and the gate triggering pulse is given by fuzzy logic controller in the feedback. Here the inverter power device circuit used is IGBT device and it has the better switching frequency and gate control compared to all other semiconductor switching devices such. This fuzzy logic control technique enables us to obtain better selective harmonic reduction in the output AC voltage. Finally the better sinusoidal wave form is obtained with minimum number of switches to get the desired level output voltage.

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